Art Unit:

**Examiner:** 



### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Chintan Desai

Serial No. [Not yet assigned]

Filed: December 21, 1999

For: RECEIVER DESERIALIZER CIRCUIT FOR FRAMING PARALLEL

DATA



CERTIFICATE OF MAILING

"Express Mail" mailing label no: EL100016184US
Date of Deposit: December 21, 1999
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1.10 on the date indicated above and is addressed to:

Box Patent Application

Assistant Commissioner for Patents Washington, D.C. 20231

Diane Morse

## TRANSMITTAL LETTER

Honorable Assistant Commissioner for Patents
Box Patent Application
Washington, D.C. 20231

Dear Sir:

Enclosed for filing please find the patent application for an invention entitled, "RECEIVER DESERIALIZER CIRCUIT FOR FRAMING PARALLEL DATA", filed on behalf of LSI Logic Corporation, assignee from inventor Chintan Desai, including Utility Patent Application Transmittal, 12 pages of specification, 5 pages of claims, 5 sheets of drawing figures, and 1 page of Abstract. Also enclosed herewith are the Combined Declaration and Power of Attorney, and Recordation Form Cover Sheet and Assignment.

The attorney's Docket Number is LSI-99-299.

Kindly address all communications regarding this application to:

Bruce R. Hoppenfeld
LSI Logic Corporation
Patent Law Department
1551 McCarthy Boulevard, M/S D-106
Milpitas, CA 95035

A check in the amount of \$760.00 is enclosed for the filing fee for a large entity, calculated as follows:

Basic Filing Fee:	\$ 760.00
No Additional Independent Claims:	\$ .00
20 Additional Dependent Claims	\$ .00
No Multiple Dependent Claims:	\$ .00
Total Filing Fee:	\$ 760.00

A check in the amount of \$40.00 is also enclosed for the Assignment fee.

In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, the Assistant Commissioner is hereby authorized to charge or credit the difference to our Deposit Account No. 04-0025. A duplicate of this page is enclosed.

Respectfully submitted, D'ALESSANDRO & RITCHIE

Dated: December 21, 1999

Nathan R. Rieth Reg. No. 44,302

D'Alessandro & Ritchie P.O. Box 640640 San Jose, CA 95164-0640 (408) 441-1100



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Respectfully submitted, D'ALESSANDRO & RITCHIE

Dated: December 21, 1999

Nathan R. Rieth Reg. No. 44,302

D'Alessandro & Ritchie P.O. Box 640640 San Jose, CA 95164-0640 (408) 441-1100 Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

## FEE TRANSMITTAI

Patent fees are subject to annual revision on October 1. These are the fees effective October 1, 1997.

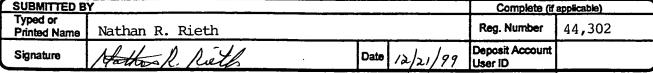
Small Entity payments must be supported by a small entity statement, otherwise large entity fees must be paid. See Forms PTO/SB/09-12. See 37 C.F.R. §§ 1.27 and 1.28.

**TOTAL AMOUNT OF PAYMENT** 

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Complete if Known		
Application Number		
Filing Date	December 21, 1999	
First Named Inventor	Chintan Desai	
Examiner Name -		
Group / Art Unit		
Attorney Docket No.	LSI-99-299	

The Commissioner is hereby authorized to charge indicated fines and credit any over payments to: Deposit Account   04-0025	METHOD OF PAYMENT (check one)	FEE CALCULATION (continued)	
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# UTILITY PATENT APPLICATION TRANSMITTAL

Attorney Docket No. LSI-99-299		
First Inventor or Application Identifier Chintan Desai		
Title Receiver Deserializer Circuit For		

(Only for new nonprovisional applications under 37 C.F.R. § 1.53(b), Express Mail Label No. EL100016184US

*	PPLICATION ELEMENTS upter 600 concerning utility patent application	contents.	Assistant Commissioner for Patents ADDRESS TO: Box Patent Application Washington, DC, 20231
14 E X I	ee Transmittal Form (e.g., PTO/SB/17) domit an original and a duplicate for fee proces		5. Microfiche Computer Program (Appendix)
	ecification :[Total Pages eferred arrangement set forth below)	18 1	Nucleotide and/or Amino Acid Sequence Submission     (if applicable, all necessary)
_	escriptive title of the Invention		a. Computer Readable Copy
	ross References to Related Application		b. Paper Copy (identical to computer copy)
	tatement Regarding Fed sponsored R & eference to Microfiche Appendix	U	c. Statement verifying identity of above copies
	ackground of the invention		ACCOMPANYING APPLICATION PARTS
- B	def Summary of the Invention		
B	rief Description of the Drawings (If filed)		A
_	etailed Description		8. (when there is an assignee) X Attorney
	laim(s)		9. English Translation Document (If applicable)
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». ∟	(for continuation/divisional with Box 16	completed)	13. Statement(s) Statement filed in prior application,
	i. DELETION OF INVENTOR(S) Signed statement attached		Certified Copy of Priority Document(s)
	inventor(s) named in the pri		(if foreign priority is claimed)
·	see 37 C.F.R. §§ 1.63(d)(2)		15. Other:
FORE FLE	LL ENTITY STATEMENT IS REQUIRED OF CER. DIN A PRIOR APPLICATION IS RELIED UPON IT	EFR \$ 1.20	***************************************
			oply the requisite information below and in a preliminary amendment:
	ontinuation Divisional Continuolication information: Examiner	uation-in-part (CIP	of prior application No:
For CONTINU	IATION or DIVISIONAL APPS only: The ent		the prior application, from which an oath or declaration is supplied
risierence. Ti	s incorporation <u>can only</u> be relied upon w	the accompanying them a portion has	ng continuation or divisional application and is hereby incorporated by us been inadvertantly omitted from the submitted application parts.
	17. COR	RESPONDEN	ICE ADDRESS
Customer Number or Bar Code Label  (Bissert Customer No. or Attach bar code label here)  Or Correspondence address below			
	Bruce R. Hoppenfeld		7700-000-0007
ISI Logic Corporation, Patent Law Department			
1551 McCarthy Boulevard			
Address	Address		
City	Milpitas	State	CA Zip Code 95035
Country	USA	Telephone	408-433-6100 Fax 408-433-7770
Name (PrintType) Nathan R. Rieth Registration No. (Attorney/Agent) 44,302			
Signatur	· Natha R. Riett	!	Date 12/21/99

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